

M5SAA

CPU Frequency Selection (JP4)

CPU	SDRAM	PCI	FS0	FS1	FS2	SD
90 MHz	90 MHz	30 MHz	Closed	Closed	Closed	Closed
95 MHz	63.3 MHz	31.7 MHz	Closed	Open	Closed	Closed
100 MHz	66.7 MHz	33.3 MHz	Open	Open	Closed	Closed
100 MHz	75 MHz	30 MHz	Closed	Closed	Open	Open
66.7 MHz	66.7 MHz	33.3 MHz	Closed	Closed	Closed	Open
75 MHz	75 MHz	30 MHz	Open	Closed	Closed	Open
83.3 MHz	83.3 MHz	33.3 MHz	Closed	Open	Closed	Open
95 MHz	95 MHz	31.7 MHz	Open	Open	Closed	Open
100 MHz	100 MHz	33.3 MHz	Closed	Closed	Open	Open

SD of JP4:

OPEN: CPU & SDRAM running at the same clock frequency.

CLOSE: SDRAM CPU and SDRAM running at individual clock frequency.

1.6.4 CPU CORE /BUS Frequency RATIO Selection (JP2/JP4)

(a) INTEL CPU

CPU Speed	Bus Clock & Multiplier	JP2 BF0	JP2 BF1	JP2 BF2	JP4 FS0	JP4 FS1	JP4 FS2	JP4 SD
100MHz	66 x 1.5	OPEN	OPEN	OPEN	CLOSE	CLOSE	CLOSE	OPEN
133MHz	66 x 2	CLOSE	OPEN	OPEN	CLOSE	CLOSE	CLOSE	OPEN
166MHz	66 x 2.5	CLOSE	CLOSE	OPEN	CLOSE	CLOSE	CLOSE	OPEN
200MHz	66 x 3	OPEN	CLOSE	OPEN	CLOSE	CLOSE	CLOSE	OPEN
233MHz	66 x 3.5	OPEN	OPEN	OPEN	CLOSE	CLOSE	CLOSE	OPEN

(b) Cyrix 6x86™ / 6x86L™ CPU

CPU Speed	Bus Clock & Multiplier	JP2 BF0	JP2 BF1	JP2 BF2	JP4 FS0	JP4 FS1	JP4 FS2	JP4 SD
PR-166+ 133MHz	66 x 2	CLOSE	OPEN	OPEN	CLOSE	CLOSE	CLOSE	OPEN
PR-200+ 150MHz	75 x 2	CLOSE	OPEN	OPEN	OPEN	CLOSE	CLOSE	OPEN

(c) Cyrix 6x86MII™ CPU

CPU Speed	Bus Clock & Multiplier	JP2 BF0	JP2 BF1	JP2 BF2	JP4 FS0	JP4 FS1	JP4 FS2	JP4 SD
PR-166+ 133MHz	66 X 2	CLOSE	OPEN	OPEN	CLOSE	CLOSE	CLOSE	OPEN
PR-200+ 150MHz	75 X 2	CLOSE	OPEN	OPEN	OPEN	CLOSE	CLOSE	OPEN
PR-200+ 166MHz	66 X 2.5	CLOSE	CLOSE	OPEN	CLOSE	CLOSE	CLOSE	OPEN
PR-233+ 166MHz	83 X 2	CLOSE	OPEN	OPEN	CLOSE	OPEN	CLOSE	OPEN
PR-233+ 188MHz	75 X 2.5	CLOSE	CLOSE	OPEN	OPEN	CLOSE	CLOSE	OPEN
PR-266+ 200MHz	66 X 3	OPEN	CLOSE	OPEN	CLOSE	CLOSE	CLOSE	OPEN
PR-233+ 200MHz	100 X 2	CLOSE	OPEN	OPEN	CLOSE	CLOSE	OPEN	OPEN
PR-266+								

(f) AMD-K6-2 and K6-3 CPU

CPU Speed	Bus clock & Multiplier	JP2 BF0	JP2 BF1	JP2 BF2	JP4 FS0	JP4 FS1	JP4 FS2	JP4 SD
266MHz	66 x 4	CLOSE	OPEN	CLOSE	CLOSE	CLOSE	CLOSE	OPEN
300MHz	66 x 4.5	CLOSE	CLOSE	CLOSE	CLOSE	CLOSE	CLOSE	OPEN
300MHz	100 x 3	OPEN	CLOSE	OPEN	CLOSE	CLOSE	OPEN	OPEN
333MHz	66 x 5	OPEN	CLOSE	CLOSE	CLOSE	CLOSE	CLOSE	OPEN
333MHz	95 x 3.5	OPEN	OPEN	OPEN	OPEN	OPEN	CLOSE	OPEN
350MHz	100 x 3.5	OPEN	OPEN	OPEN	CLOSE	CLOSE	OPEN	OPEN
366MHz	66 x 5.5	OPEN	OPEN	CLOSE	CLOSE	CLOSE	CLOSE	OPEN
380MHz	95 x 4	CLOSE	OPEN	CLOSE	OPEN	OPEN	CLOSE	OPEN
400MHz	100 x 4	CLOSE	OPEN	CLOSE	CLOSE	CLOSE	OPEN	OPEN
450MHz	100 x 4.5	CLOSE	CLOSE	CLOSE	CLOSE	CLOSE	OPEN	OPEN

(g) Rise™ -MP6 CPU

CPU Speed	Bus Clock& Multiplier	JP2 BF0	JP2 BF1	JP2 BF2	JP4 FS0	JP4 FS1	JP4 FS2	JP4 SD
166MHz	83 x 2	CLOSE	OPEN	OPEN	CLOSE	OPEN	CLOSE	OPEN
190MHz	95 x 2	CLOSE	OPEN	OPEN	OPEN	OPEN	CLOSE	OPEN
200MHz	100 x 2	CLOSE	OPEN	OPEN	CLOSE	CLOSE	OPEN	OPEN




(h) Idt-Win™ 2A Chip CPU

CPU Speed	Bus Clock & Multiplier	JP2 BF0	JP2 BF1	JP2 BF2	JP4 FS0	JP4 FS1	JP4 FS2	JP4 SD
PR-200+ 200MHz	66 x 3	OPEN	CLOSE	OPEN	CLOSE	CLOSE	CLOSE	OPEN
PR-233+ 200MHz	100 x 2	CLOSE	OPEN	OPEN	CLOSE	CLOSE	OPEN	OPEN
PR-233+ 208MHz	83 x 2.5	CLOSE	CLOSE	OPEN	CLOSE	OPEN	CLOSE	OPEN
PR-233+ 233MHz	66 x 3.5	OPEN	OPEN	OPEN	CLOSE	CLOSE	CLOSE	OPEN
PR-266+ 233MHz	100 x 2.33	OPEN	CLOSE	CLOSE	CLOSE	CLOSE	OPEN	OPEN
PR-266+ 250MHz	83 x 3	OPEN	CLOSE	OPEN	CLOSE	OPEN	CLOSE	OPEN
PR-300+ 250MHz	100 x 2.5	CLOSE	CLOSE	OPEN	CLOSE	CLOSE	OPEN	OPEN
PR-266+ 266MHz	66 x 4	CLOSE	OPEN	CLOSE	CLOSE	CLOSE	CLOSE	OPEN
PR-300+ 266MHz	100 x 2.66	OPEN	OPEN	CLOSE	CLOSE	CLOSE	OPEN	OPEN

(i) Idt-Win™ 3 Chip CPU

CPU Speed	Bus Clock & Multiplier	JP2 BF0	JP2 BF1	JP2 BF2	JP4 FS0	JP4 FS1	JP4 FS2	JP4 SD
PR-233+ 200MHz	66 x 3	OPEN	CLOSE	OPEN	CLOSE	CLOSE	CLOSE	OPEN
PR-266+ 233MHz	66 x 3.5	OPEN	OPEN	OPEN	CLOSE	CLOSE	CLOSE	OPEN
PR-300+ 233MHz	100 x 2.33	OPEN	CLOSE	CLOSE	CLOSE	CLOSE	OPEN	OPEN
PR-300+ 266MHz	66 x 4	CLOSE	OPEN	CLOSE	CLOSE	CLOSE	CLOSE	OPEN
PR-333+ 266MHz	100 x 2.66	OPEN	OPEN	CLOSE	CLOSE	CLOSE	OPEN	OPEN
PR-350+ 285MHz	95 x 3	OPEN	CLOSE	OPEN	OPEN	OPEN	CLOSE	OPEN
PR-333+ 300MHz	66 x 4.5	CLOSE	CLOSE	CLOSE	CLOSE	CLOSE	CLOSE	OPEN
PR-366+ 300MHz	100 x 3	OPEN	CLOSE	OPEN	CLOSE	CLOSE	OPEN	OPEN
PR-380+ 316MHz	95 x 3.33	CLOSE	OPEN	OPEN	OPEN	OPEN	CLOSE	OPEN
PR-366+ 333MHz	66 x 5	OPEN	CLOSE	CLOSE	CLOSE	CLOSE	CLOSE	OPEN
PR-400+ 333MHz	100 x 3.33	CLOSE	OPEN	OPEN	CLOSE	CLOSE	OPEN	OPEN

CMOS Function Selection (JP7)

JP7	Assignment
 1-2 Closed	Normal Operation (default)
 2-3 Closed	Clear CMOS Data (*Note)
 Open	Onboard Battery Disabled

Note : Please follow the procedure as below to clear CMOS Data.

Note : Please follow the procedure as below to clear BIOS Password if your password is lost or forgotten.

